HZ0402A152R-10

UNCONTROLLED DOCUMENT

PHYSICAL DIMENSIONS:

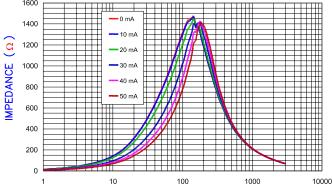
A 1.00 [.040] ± 0.15 [.006]

B 0.50 [.020] ± 0.15 [.006]

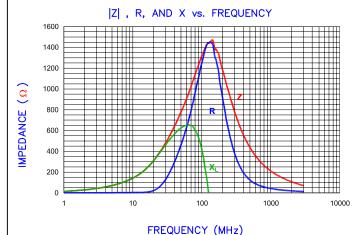
C 0.50 [.020] ± 0.15 [.006]

D 0.30 [.012] MAX.

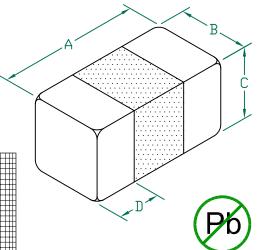
Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)



AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 6231

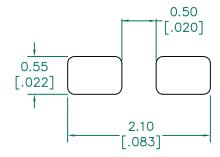


ELECTRICAL CHARACTERISTICS:								
Z @ 100MHz (Ω)		DCR (Ω)	Rated Current					
Nominal	1500							
Minimum	1125							
Maximum	1875	2.00	50 mA					

NOTES: UNLESS OTHERWISE SPECIFIED

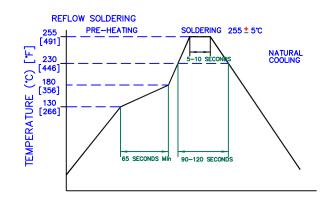
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 5. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 (.030 to this dimension.)

RECOMMENDED SOLDERING CONDITIONS





	DIMENSIONS ARE IN mm [INCHES].			This print is the property of Laird					
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				copies shall be made without the		Laird			
				written consent of Laird Tech. All					
				rights to design or invention are					
				reserved.					
				PROJECT/PART NUMBER:	REV	PART TYPE:	DRAWN BY:		
С	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	HZ0402A152R-10	С	CO-FIRE	JRK		
В	UPDATE COMPANY LOGO	02/15/08	JRK	DATE: 07/24/07 sc	ALE: N	TS SHEET:	!		
Α	ORIGINAL DRAFT	07/24/07	JRK	CAD # 1707	OL #		of 2		
REV	DESCRIPTION	DATE	INT	CAD # HZ0402A152R-10-C	··· .	- -	01 2		