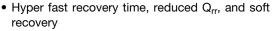
# Hyper Fast Rectifier, 2 x 3 A FRED Pt®



3.4 • • 5,6

PRODUCT SUMMARY				
Package	FlatPAK 5 x 6			
I <sub>F(AV)</sub>	2 x 3 A			
$V_{R}$	200 V			
V <sub>F</sub> at I <sub>F</sub>	0.71 V			
t <sub>rr</sub>	25 ns			
T <sub>J</sub> max.	175 °C			
Diode variation	Separated cathode			

#### **FEATURES**





HALOGEN

**FREE** 

• 175 °C maximum operating junction temperature

Low forward voltage drop

· Low leakage current

- Specific for output and snubber operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art hyper fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

### **MECHANICAL DATA**

Case: FlatPAK 5 x 6

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant M3 suffix meets JESD 201 class 2 whisker test

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		$V_{RRM}$		200		
Average rectified forward current per	per device	I <sub>F(AV)</sub>	T <sub>Solderpad</sub> = 170 °C, DC	3	V	
Average rectified forward current per	i device		T <sub>Solderpad</sub> = 169 °C, D = 0.5	3		
Non-repetitive peak surge current per	r device	1	T <sub>J</sub> = 25 °C, 10 ms sinusoidal pulse	147	Α	
pe	er diode	IFSM		70	A	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	200	-	-	
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 3 A	-	0.88	0.94	V
		I <sub>F</sub> = 3 A, T <sub>J</sub> = 150 °C	-	0.71	0.74	
Reverse leakage current	I <sub>R</sub>	$V_R = V_R$ rated	-	-	2	μA
		$T_J = 150  ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	6	40	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	14	-	pF



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	26	-	
Poverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	25	
Reverse recovery time		T <sub>J</sub> = 25 °C	$I_F = 3 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	15	-	ns A nC
		T <sub>J</sub> = 125 °C		-	25	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	2	-	
		T <sub>J</sub> = 125 °C		-	3	-	
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	12	-	
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	40	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to ambient	R <sub>thJA</sub> (1)(2)		-	90	103	
Thermal resistance, junction to case	R <sub>thJC</sub> (3)		-	2.3	2.6	°C/W

#### **Notes**

- $^{(1)}$  The heat generated must be less than thermal conductivity from junction to ambient;  $dP_D/dT_J < 1 \text{ x R}_{thJA}$
- (2) Free air, mounted or recommended copper pad area; thermal resistance R<sub>thJA</sub> junction to ambient
- (3) Mounted on infinite heatsink

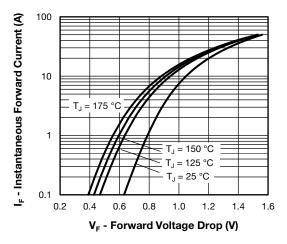


Fig. 1 - Typical Forward Voltage Drop Characteristics

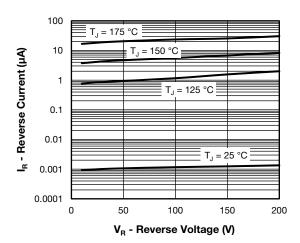


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

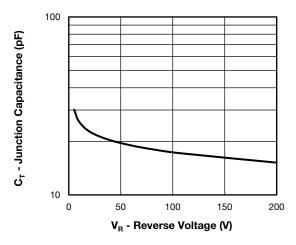


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

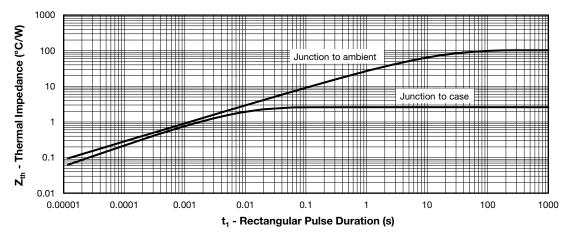


Fig. 4 - Maximum Thermal Impedance Zth Characteristics

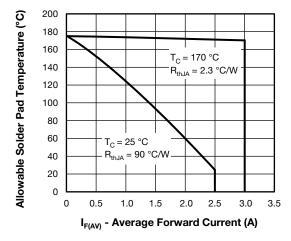


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

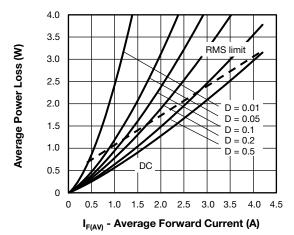


Fig. 6 - Forward Power Loss Characteristics

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# Vishay Semiconductors

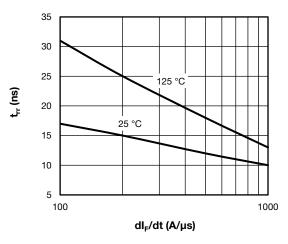


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

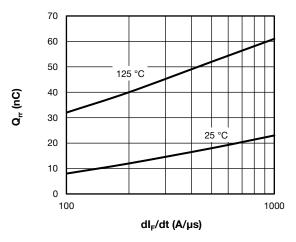
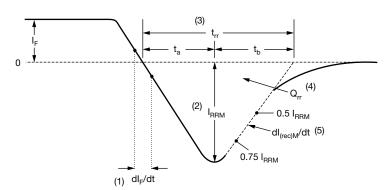


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $^{(1)}$  Formula used:  $T_C = T_J$  - (Pd + Pd\_{REV}) x  $R_{thJC}$ ; Pd = forward power loss =  $I_{F(AV)}$  x  $V_{FM}$  at ( $I_{F(AV)}/D$ ) (see Fig. 6); Pd\_{REV} = inverse power loss =  $V_{R1}$  x  $I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = rated  $V_R$ 



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $Q_{rr}$  area under curve defined by  $t_{rr}$  and  $I_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

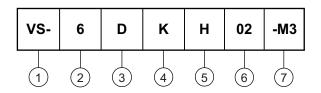
(5) dI<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions



### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (6 = 6 A)

Circuit configuration:

D = separated cathode

K = FlatPAK package

5 - Process type:

H = hyperfast recovery

6 - Voltage code (02 = 200 V)

7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION	
VS-6DKH02-M3/H	0.10	Н	1500	7"diameter plastic tape and reel	
VS-6DKH02-M3/I	0.10	I	6000	13"diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96056</u>				
Part marking information <u>www.vishay.com/doc?96059</u>				
Packaging information	www.vishay.com/doc?88869			



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