# DC-DC Converter (-20V, -3.5A)

# RTQ035P02

#### Features

- 1) Low On-resistance.( $80m\Omega$  at 2.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive.(2.5V)

### Applications

DC-DC converter

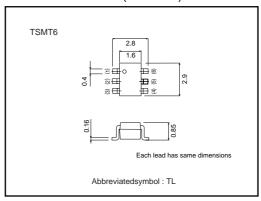
#### ●Structure

Silicon P-channel **MOSFET** 

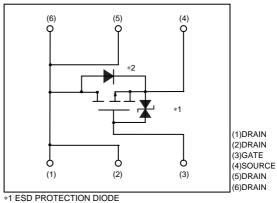
#### Packaging specifications

|           | Package                      | Taping |
|-----------|------------------------------|--------|
| Туре      | Code                         | TR     |
|           | Basic ordering unit (pieces) | 3000   |
| RTQ035P02 |                              | 0      |

#### ●External dimensions (Units : mm)



#### ●Equivalent circuit



- \*2 BODY DIODE

**ROHM** 

# ● Absolute maximum ratings (Ta=25°C)

| Parameter                    |            | Symbol | Limits            | Unit |
|------------------------------|------------|--------|-------------------|------|
| Drain-source voltage         |            | Voss   | -20               | V    |
| Gate-source voltage          |            | Vgss   | ±12               | V    |
| Desir summer                 | Continuous | lo     | ±3.5              | A    |
| Drain current                | Pulsed     | IDP    | ±17.5             | A *1 |
| Source current               | Continuous | ls     | -1                | A    |
| (Body diode)                 | Pulsed     | Isp    | -4                | A *1 |
| Total power dissipation      |            | Po     | 1.25              | W*2  |
| Channel temperature          |            | Tch    | 150               | °C   |
| Range of Storage temperature |            | Tstg   | -55 <b>~</b> +150 | °C   |

<sup>\*1</sup> Pw≦10μs, Duty cycle≦1% \*2 Mounted on a ceramic board

# ●Electrical characteristics (Ta=25°C)

| Parameter                                  | Symbol           | Min.     | Тур.   | Max. | Unit | Conditions  |  |
|--|------------------|----------|--------|------|------|---|--|
| Gate-source leakage                        | Igss             | -        | -      | ±10  | μΑ   | Vgs=±12V, Vds=0V                                    |  |
| Drain-source breakdown voltage             | V(BR)DSS         | -20      | _      | _    | V    | ID=-1mA, VGS=0V                                     |  |
| Zero gate voltage drain current            | IDSS             | _        | _      | -1   | μΑ   | VDS=-20V, VGS=0V                                    |  |
| Gate threshold voltage                     | VGS(th)          | -0.7     | _      | -2.0 | V    | V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA         |  |
| Static drain-source on-state resistance    | RDS(on)          | _        | 50     | 65   | mΩ   | ID=-3.5A, VGS=-4.5V                                 |  |
|  |                  | _        | 55     | 70   | mΩ   | ID=-3.5A, VGS=-4V                                   |  |
|  |                  | _        | 80     | 100  | mΩ   | ID=-1.75A, VGS=-2.5V                                |  |
| Foward transfer admittance                 | Yfs *            | 3.5      | _      | _    | S    | V <sub>D</sub> S=-10V, I <sub>D</sub> =-3.5A        |  |
| Input capacitance                          | Ciss             | _        | 1200   | _    | pF   |   |  |
| Output capacitance                         | Coss             | -        | 200    | _    | pF   | V <sub>DS</sub> =-10V,V <sub>GS</sub> =0V<br>f=1MHz |  |
| Reverse transfer capacitance               | Crss             | -        | 130    | _    | pF   |   |  |
| Turn-on delay time                         | td(on) *         | -        | 16     | _    | ns   | - Ip=-2A  |  |
| Rise time                                  | tr *             | -        | 40     | _    | ns   | - 10=-2A<br>  VDD≒-15V                              |  |
| Turn-off delay time                        | td(off) *        | _        | 55     | _    | ns   | Vgs=-4.5V<br>R <sub>L</sub> =7.5Ω                   |  |
| Fall time                                  | t <sub>f</sub> * | _        | 30     | _    | ns   | $R_{GS}=10\Omega$                                   |  |
| Total gate charge                          | Qg               | _        | 10.5   | _    | nC   | .,  |  |
| Gate-source charge                         | Qgs              | _        | 2.0    | _    | nC   | V <sub>DD</sub> =-15V<br>V <sub>GS</sub> =-4.5V     |  |
| Gate-drain charge                          | Qgd              | _        | 3.5    | _    | nC   | ID=-3.5A  |  |
| *PULSED Body diode characteristics (source | e-drain ch       | aracteri | stics) | •    |      |   |  |
|  | \ (O.D.          |          | ,      | 4.0  | ٠,,  | 1. 4.4.37 037                                       |  |

| Forward voltage | Forward voltage | VSD | _ | _ | -1.2 | V | Is=-1A, Vgs=0V |
|-----------------|-----------------|-----|---|---|------|---|----------------|
|-----------------|-----------------|-----|---|---|------|---|----------------|

#### Electrical characteristic curves

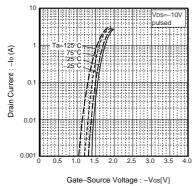


Fig.1 Typical Transfer Characteristics

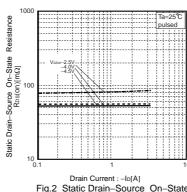


Fig.2 Static Drain–Source On–State Resistance vs. Drain Current

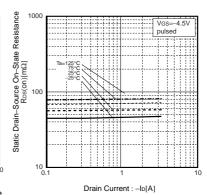


Fig.3 Static Drain–Source On–State Resistance vs.Drain Current

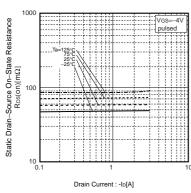


Fig.4 Static Drain–Source On–State Resistance vs. Drain–Current

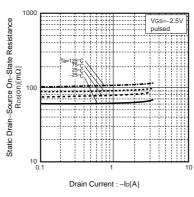


Fig.5 Static Drain–Source On–State Resistance vs.Drain–Current

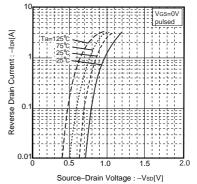


Fig.6 Reverse Drain Current vs. Source-Drain Voltage

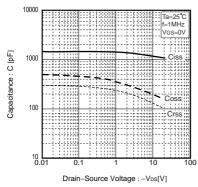


Fig.7 Typical Capactitance vs.Drain–Source Voltage

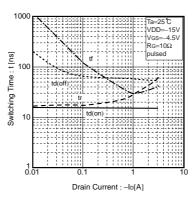


Fig.8 Switching Characteristics

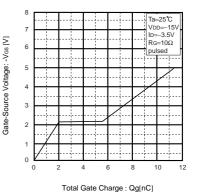


Fig.9 Dynamic Input Characteristics

#### Measurement circuits

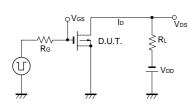


Fig.10 Switching Time Measurement Circuit

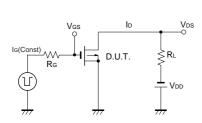


Fig.12 Gate Charge Measurement Circuit

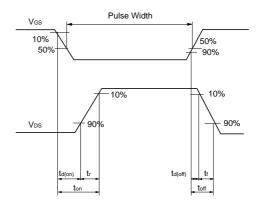


Fig.11 Switching Waveforms

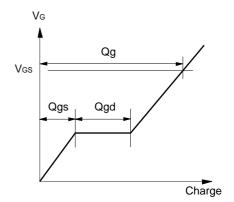


Fig.13 Gate Charge Waveforms

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