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MMBFJ271 P-Channel Switch

June 2006

Features

- This device is designed for low level analog switching sample and hold circuits and chopper stabilized amplifiers.
- · Sourced from process 88.



Absolute Maximum Ratings * T_a = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	-30	V
V _{GS}	Gate-Source Voltage	30	V
I _{GF}	Forward Gate Current	50	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

Thermal Characteristics

Symbol	Parameter	Value	Units
P_{D}	Total Device Dissipation Derate above 25°C	225 1.8	mW mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	556	°C/W

Note2 : Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch

Electrical Characteristics T_C = 25°C unless otherwise noted

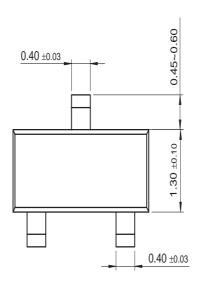
Symbol	Parameter	Test Condition	MIN	MAX	Units
Off Charac	eteristics (Note3)			•	
V _{(BR)GSS}	Gate-Source Breakdwon Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	30		V
I _{GSS}	Gate Reverse Current	$V_{GS} = 20V, V_{DS} = 0$		200	pA
V _{GS(off)}	Gate-Source Cutoff Voltage	$V_{DS} = -15V, I_{D} = -1.0nA$	1.5	4.5	V
On Charac	teristics (Note3)				
I _{DSS}	Zero-Gate Voltage Drain Current *	V _{DS} = -15V, V _{GS} = 0	-6.0	-50	mA
gfs	Forward Transferconductance	$V_{GS} = 0V, V_{DS} = 15V, f = 1.0kHz$	8000	18000	μmhos
goss	Common- Source Output Conductance	$V_{GS} = 0V, V_{DS} = 15V, f = 1.0kHz$		500	μmhos

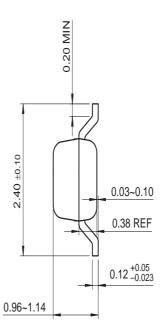
Note3: Short duration test pulse used to minimize self-heating effect.

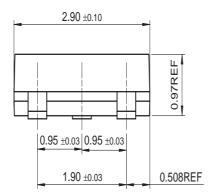
⁻ These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

Package Dimensions

SOT-23







Dimensions in Millimeters

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