() IDT.

QUICKSWITCH[®] PRODUCTS HIGH-SPEED CMOS QUICKSWITCH 16:8 MULTIPLEXER

IDTQS32390

FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- 16:8 multiplexer function with zero delay
- 5 Ω bidirectional switches connect inputs to outputs
- · Zero propagation delay, zero ground bounce
- · Undershoot clamp diodes on all switch and control inputs
- Direct bidirectional connection for mux, demux
- 25Ω resistors for low noise
- Available in QSOP package

DESCRIPTION:

The QS32390 provides a 16:8 multiplexer logic switch. The QS32390 has an internal 25Ω resistor to reduce reflection noise in high-speed applications. The enable inputs connect one of two inputs to the common I/O pin, respectively. The multiplexer function can be used to select and route logic signals for zero delay, isolate bus capacitance, form crossbar switches, etc. Mux/Demux devices provide an order of magnitude faster speed than

equivalent logic devices.

The QS32390 is characterized for operation at -40°C to +85°C.

APPLICATIONS:

- · Video, audio, graphics switching, muxing
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)

FUNCTIONAL BLOCK DIAGRAM



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INDUSTRIAL TEMPERATURE RANGE

FEBRUARY 2011

INDUSTRIAL TEMPERATURE RANGE

PIN CONFIGURATION



QSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	–0.5 to +7	V
VTERM ⁽³⁾	DC Switch Voltage Vs	–0.5 to +7	V
VTERM ⁽³⁾	DC Input Voltage VIN	–0.5 to +7	V
VAC	AC Input Voltage (pulse width \leq 20ns)	-3	V
Ιουτ	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (TA = 85°C)	0.5	W
TSTG	Storage Temperature	–65 to +150	°C

NOTE:

 Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

2. Vcc terminals.

3. All terminals except Vcc.

CAPACITANCE

 $(TA = +25^{\circ}C, f = 1.0MHz, VIN = 0V, VOUT = 0V)$

Pins		Тур.	Max. ⁽¹⁾	Unit
Control Pins		4	5	рF
Quickswitch Channels	Demux	5	7	рF
(Switch OFF)	Mux	9	10	

NOTE:

1. This parameter is measured at characterization but not tested.

PIN DESCRIPTION

Pin Names	I/O	Description
A0 - A9 I/O		Bus A
B0 - B9	I/O	Bus B
AEN, BEN		Bus Switch Enable

FUNCTION TABLE(1)

AEN	BEN	A0 - A9	B0 - B9	Function
Н	Н	Off	Off	Disconnect
L	Н	On	Off	A to AB
Н	L	Off	On	B to AB
L	L	On	On	A, B to AB

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified: Industrial: TA = -40°C to +85°C, Vcc = $5.0V \pm 5\%$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
Vih	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	_	_	V
Vil	Input LOW Level	Guaranteed Logic LOW for Control Pins	_	—	0.8	V
lin	Input LeakageCurrent (Control Inputs) ⁽²⁾	$0V \le VIN \le VCC$	_	_	±1	μA
loz	Off-State Output Current (Hi-Z)	$0V \le VOUT \le VCC$, Switches OFF	_	_	±1	μA
Ron ⁽³⁾	Switch ON Resistance	Vcc = Min., VIN = 0V, ION = 30mA	15	20	35	Ω
		Vcc = Min., VIN = 2.4V, ION =15mA	15	19	40	
Vp	Pass Voltage ⁽²⁾	$V_{IN} = V_{CC} = 5V$, Iout = $-5\mu A$	3.7	4	4.2	V

NOTES:

1. Typical values are at Vcc = 5.0V, TA = 25°C.

2. Pass Voltage is guaranteed but not production tested.

3. Rout changed on March 8, 2002. See rear page for more information.

TYPICAL ON RESISTANCE vs Vin AT Vcc = 5V



(Volts)

POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
lccq	Quiescent Power Supply Current	Vcc = Max., VIN = GND or Vcc, f = 0	3	μA
Δlcc	Power Supply Current per Control Input HIGH (2)	Vcc = Max., VIN = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz ⁽³⁾	Vcc = Max., A, B, and AB pins open	0.25	mA/MHz
		Control Inputs Toggling at 50% Duty Cycle		

NOTES:

1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.

2. Per TLL driven input (VIN = 3.4V, control inputs only). A, B, and AB pins do not contribute to ∆Icc.

3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A, B, and AB I/Os generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40^{\circ}C$ to +85°C, Vcc = 5.0V ± 5%;

CLOAD = 50pF, RLOAD = 500Ω unless otherwise noted.

Symbol	Parameter	Min. ⁽¹⁾	Тур.	Max.	Unit
tPLH	Data Propagation Delay ^(2,4)	—	—	1.25 ⁽³⁾	ns
tPHL	AxBx to CxDx, CxDx to AxBx				
tРZH	Switch Turn-on Delay	1.5	—	7.5	ns
tPZL	BE to Ax, Bx, Cx, Dx				
tPHZ	Switch Turn-off Delay ⁽²⁾	—	—	5.5	ns
tPLZ	BE to Ax, Bx, Cx, Dx				

NOTES:

1. Minimums are guaranteed but not production tested.

2. This parameter is guaranteed but not production tested.

3. The time constant for the switch alone is of the order of 1.25ns for $C_L = 50 pF$.

4. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



As per PCN L0201-02, the Output Resistance (Ron) specifications have changed as of March 8, 2002. The original specifications were:

Parameter	Description	Min.	Тур.	Max.	Unit
Ron	Vcc = Min, VIN = 0V, ION = 30mA	20	28	40	Ω
	Vcc = Min, VIN = 2.4V, ION = 15mA	24	35	48	

Datasheet Document History

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02/09/2011

Updated the ordering information by removing the "IDT" notation, non RoHS part and by adding Tape and Reel information.



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